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Zhang has published more than 60 papers at international conferences and in technical journals and is the editor of Embedded Memory for Nano-Scale VLSIs, published by Springer in 2009. He holds more than 50 U.S. patents in the field of integrated circuit technology. Zhang is 2016 ISSCC Program chair and also serves on IEEE VLSI Executive Committee.

Circuit Design in Nano-Scale CMOS Technologies | DeepDyve

- Nano: NXP i.MX 8M Nano processor with up to four ARM® Cortex™-A53 cores running up to 1.5 GHz, plus an ARM® Cortex™-M7 core running up to 750 MHz, GPU (GC7000UL) Embedded Memory
- Mini: Up to 8GB of 32-bit wide LPDDR4 memory
- Nano: Up to 4GB of 16-bit wide LPDDR4 memory
- eMMC, configurable
- Quad SPI NOR Flash, configurable

i.MX 8M MINI/NANO SOM - beaconembedded.com

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proteanTecs Granted US Patent for High Bandwidth Memory ...

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